Amendments to the Claims:

The listing of claims will replace all prior versions, and listings, of claims in the application. Claims 3, 5, 11, 13, 17 and 19 have been cancelled without disclaimers, and claims 1, 2, 9, 10, 15 and 16 have been amended without adding new matters. Entry and consideration of the claim amendments is respectfully requested.

Listings of Claims:

1. (Currently Amended) An electroluminescent display comprising;

at least one scan line;

a plurality of data lines;

a plurality of light-emitting devices arranged in a row along the at least one scan line, wherein each light-emitting device has a light emission region that forms a display area; and

a plurality of electrical addressing devices respectively coupling each of the light-emitting devices in the row to one data line and the at least one scan line;

wherein the entire display areas of at least two light-emitting devices adjacent to each other in the row ~~include display areas~~ are arranged at alternated sides of the at least one scan line ~~according to a nonuniform distribution along the at least one scan line~~.

2. (Currently Amended) The electroluminescent display of claim 1, wherein the display areas of at least two light-emitting devices adjacent to each other in the row are arranged at oppositely offset positions relative to the row direction ~~offset from one another in a direction approximately perpendicular to the at least one scan line~~.

3. (Cancelled)

4. (Original) The electroluminescent display of claim 1, wherein the at least one scan line forms a crenelated profile.

5. (Cancelled)

6. (Original) The electroluminescent display of claim 1, wherein the light-emitting devices include organic light-emitting diodes.

7. (Original) The electroluminescent display of claim 1, wherein one or more of the electrical addressing devices includes the coupling of a switch thin film transistor and a driver thin film transistor.

8. (Original) The electroluminescent display of claim 7, wherein the switch thin film transistor is coupled to the at least one scan line and one data line to respectively receive addressing and image signals, and the driver thin film transistor is coupled to the switch thin film transistor to deliver an electric current to one light-emitting device.

9. (Currently Amended) A liquid crystal display comprising:

at least one scan line;

a plurality of data lines;

a plurality of display electrodes arranged in a row along the scan line; and

a plurality of electrical addressing devices respectively coupling each display electrode in the row to one data line and the at least one scan line;

wherein ~~the~~ at least two display electrodes adjacent to each other in the row are entirely arranged ~~according to a nonuniform distribution along~~ at alternated sides of the at least one scan line.

10. (Currently Amended) The liquid crystal display of claim 9, wherein ~~the~~ two display electrodes adjacent to each other in the row are arranged at oppositely offset positions relative to the row direction ~~from one another in a direction approximately perpendicular to the at least one scan line~~.

11. (Cancelled)

12. (Original) The liquid crystal display of claim 9, wherein the at least one scan line forms a crenelated profile.

13. (Cancelled)

14. (Original) The liquid crystal display of claim 9, wherein one or more of the electrical addressing devices includes a thin film transistor having a gate terminal connected to the at least one scan line, a drain terminal connected to one data line, and a source terminal connected to one display electrode.

15. (Currently Amended) A display pixel array comprising:

at least one scan line;

a plurality of data lines;

a plurality of pixels, each pixel including one or more color subpixels arranged in a row along the at least one scan line, and each subpixel having a display area; and

a plurality of electrical addressing devices respectively coupling each color subpixel in the row to one data line and the at least one scan line;

wherein the ~~color subpixels include~~ entire display areas of two color subpixels adjacent to each other in the row are arranged ~~according to a nonuniform distribution along~~ at alternated sides of the at least one scan line.

16. (Currently Amended) The pixel array of claim 15, wherein the display areas of two color subpixels adjacent to each other in the row are arranged at oppositely offset positions relative to the row direction ~~from one another in a direction approximately perpendicular to the at least one scan line~~.

17. (Cancelled)

18. (Original) The pixel array of claim 15, wherein the at least one scan line forms a crenelated profile.

19. (Cancelled)

20. (Original) The pixel array of claim 15, wherein one or more color subpixels respectively includes one display electrode coupled with one electrical addressing device.

21. (Original) The pixel array of claim 20, wherein one or more electrical addressing device respectively includes a thin film transistor having a gate terminal connected to the at least one scan line, and a drain terminal connected to one data line, and a source terminal connected to one display electrode.

22. (Original) The pixel array of claim 15, wherein one or more subpixels respectively include one light-emitting device coupled to one electrical addressing device.

23. (Original) The pixel array of claim 22, wherein one or more electrical addressing devices respectively includes the coupling of a switch thin film transistor and a driver thin film transistor.

24. (Original) The pixel array of claim 23, wherein the switch thin film transistor is coupled to the at least one scan line and one data line to respectively receive addressing and image signals, and the driver thin film transistor is coupled to the switch thin film transistor to deliver an electric current to one light-emitting device.

25. (Original) The pixel array of claim 15, wherein the pixels are arranged according to a delta configuration.